With its global logistics network and strong portfolio of power semiconductor devices, ON Semiconductor (Nasdaq: ONNN) is a preferred supplier of power solutions to engineers, purchasing professionals, distributors and contract manufacturers in the power supply, computer, consumer, portable/wireless, automotive and industrial markets. ON Semiconductor is headquartered in Phoenix, USA, and our hubs in the Czech Republic are Rožnov pod Radhoštěm and in Brno.

Due to the continuous expansion of the Brno design center we are offering this excellent job opportunity:

**DIGITAL DESIGN ENGINEER**

**YOU WILL**

- design digital parts of mixed-signal integrated circuits and code these in VHDL, Verilog, SystemVerilog
- simulate the designs using tools from leading EDA vendors (e.g. Cadence, Synopsys, Mentor)
- verify designs ranging from blocks to complete system-on-chip mixed-signal ICs using latest verification methodologies
- be involved in all aspects of the development cycle from specification through tape-out to qualification for production

**WE LOOK FOR**

- university education in electronics
- min. 3-years of experiences in ASIC/FPGA design
- knowledge of HDL languages (e.g. Verilog, System Verilog, VHDL), synthesis (synopsys), FPGA
- experience with design of integrated circuits
- strong motivation for technical challenges
- good communication skills
- active English

**WE OFFER**

- career in a successful international company
- challenging work environment
- participation in interesting international projects
- wide range of training possibilities
- possibility of professional growth
- above standard company benefits (5 weeks of vacation, flexible working time, rental allowance, life insurance contribution etc.)

If you are interested in the position, please send us your CV in English to brno.careers@onsemi.com.

For more information and other currently open positions see www.onsemi.jobs.cz.

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